

Exposure of the inner layers and transfer of the pattern

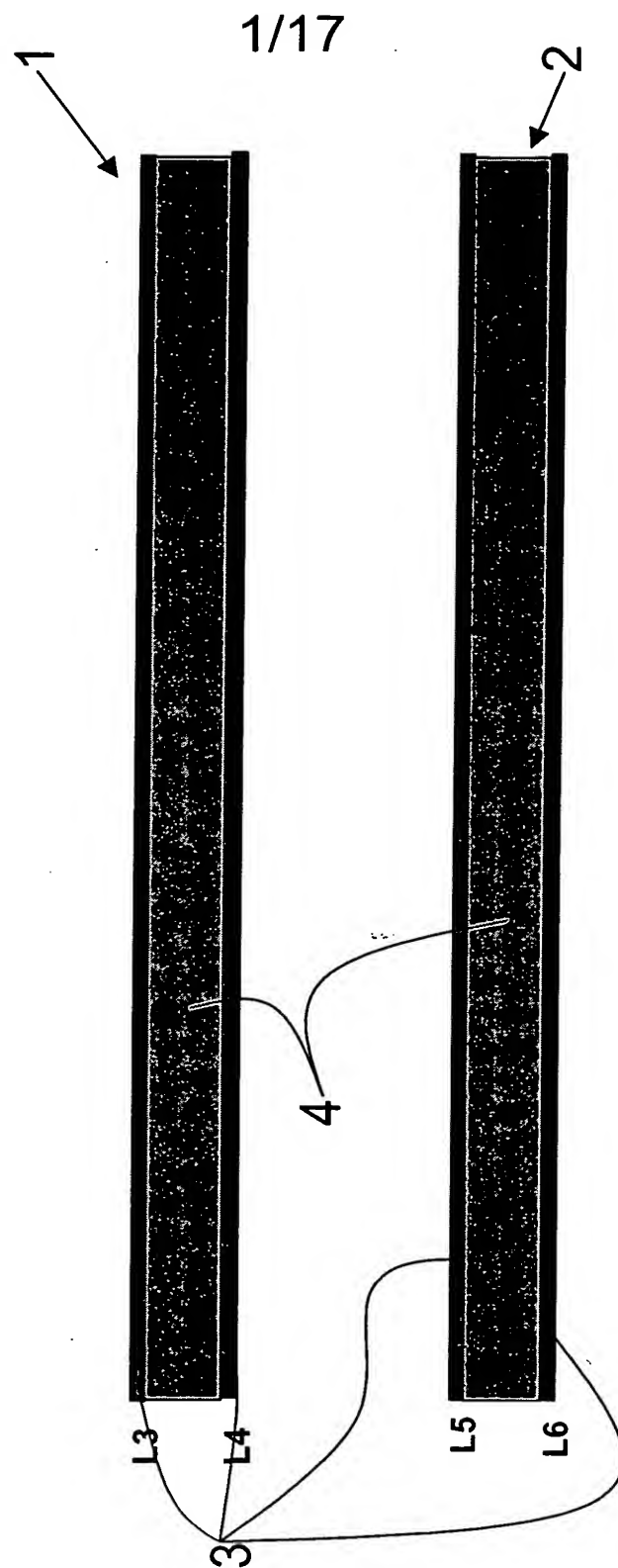


Fig. 1

Etching of unexposed part and removal of protection

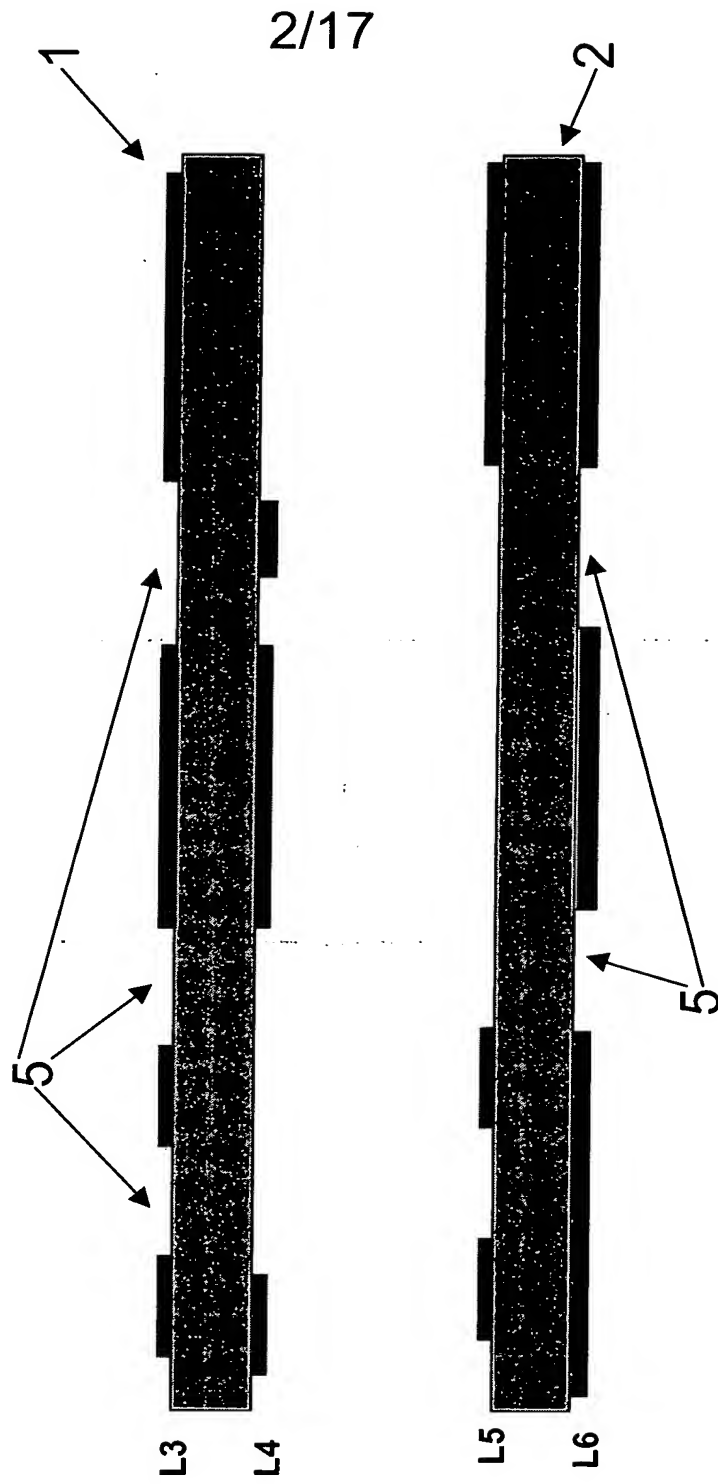


Fig. 2

# Insulation and pressing of the layers

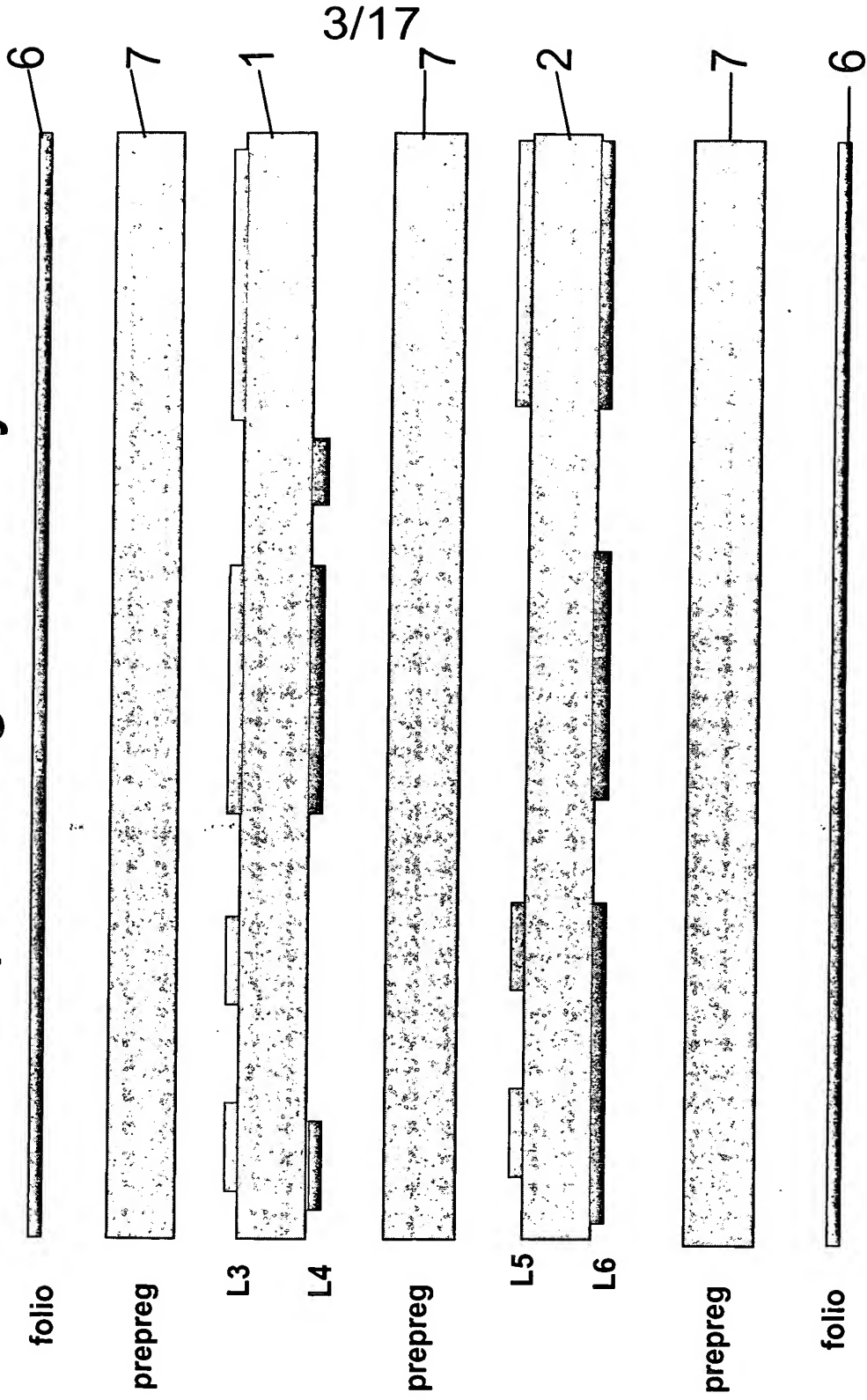


Fig. 3

# Through-drilling of the inner layers

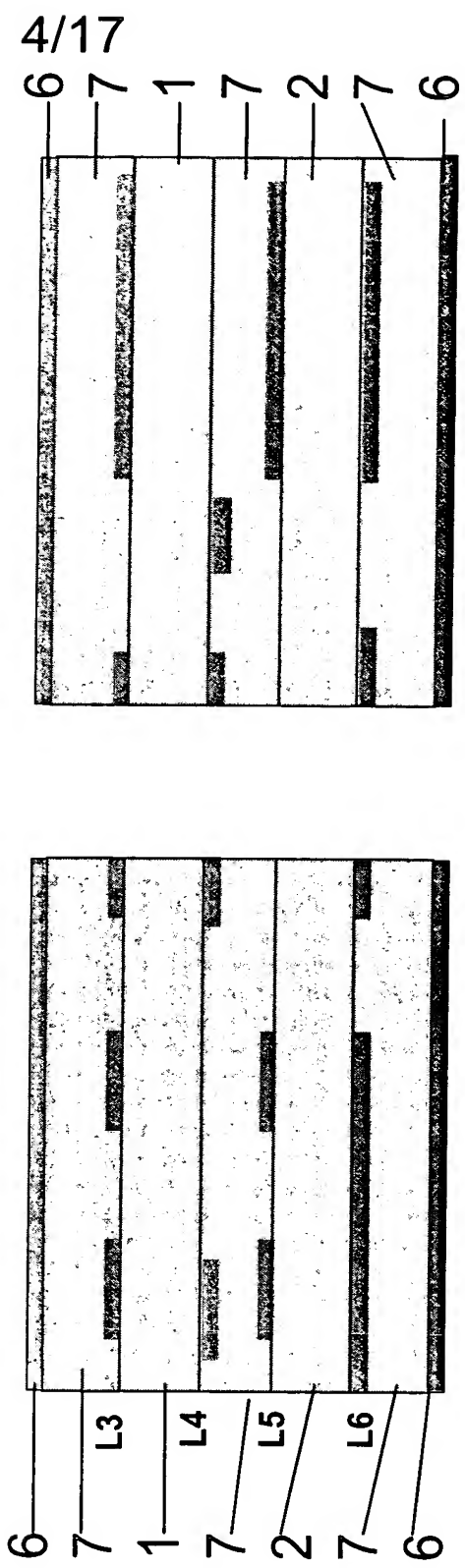


Fig. 4

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Plating of the through holes to make  
them conducting

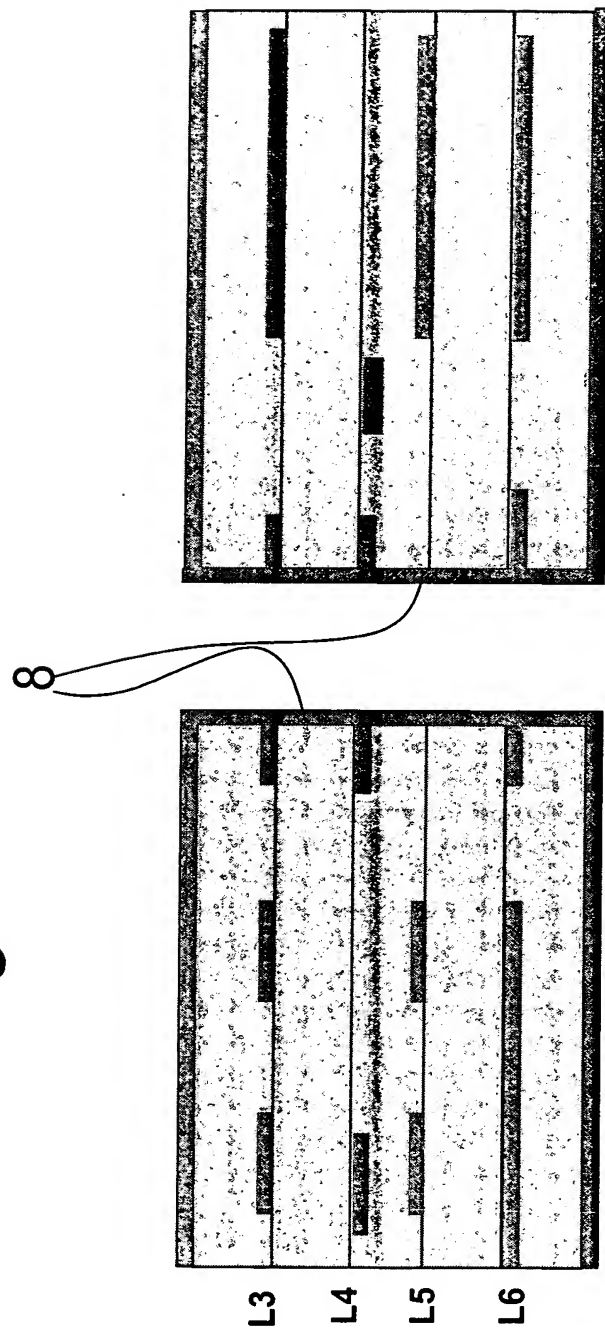


Fig. 5

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Removal of unnecessary plating

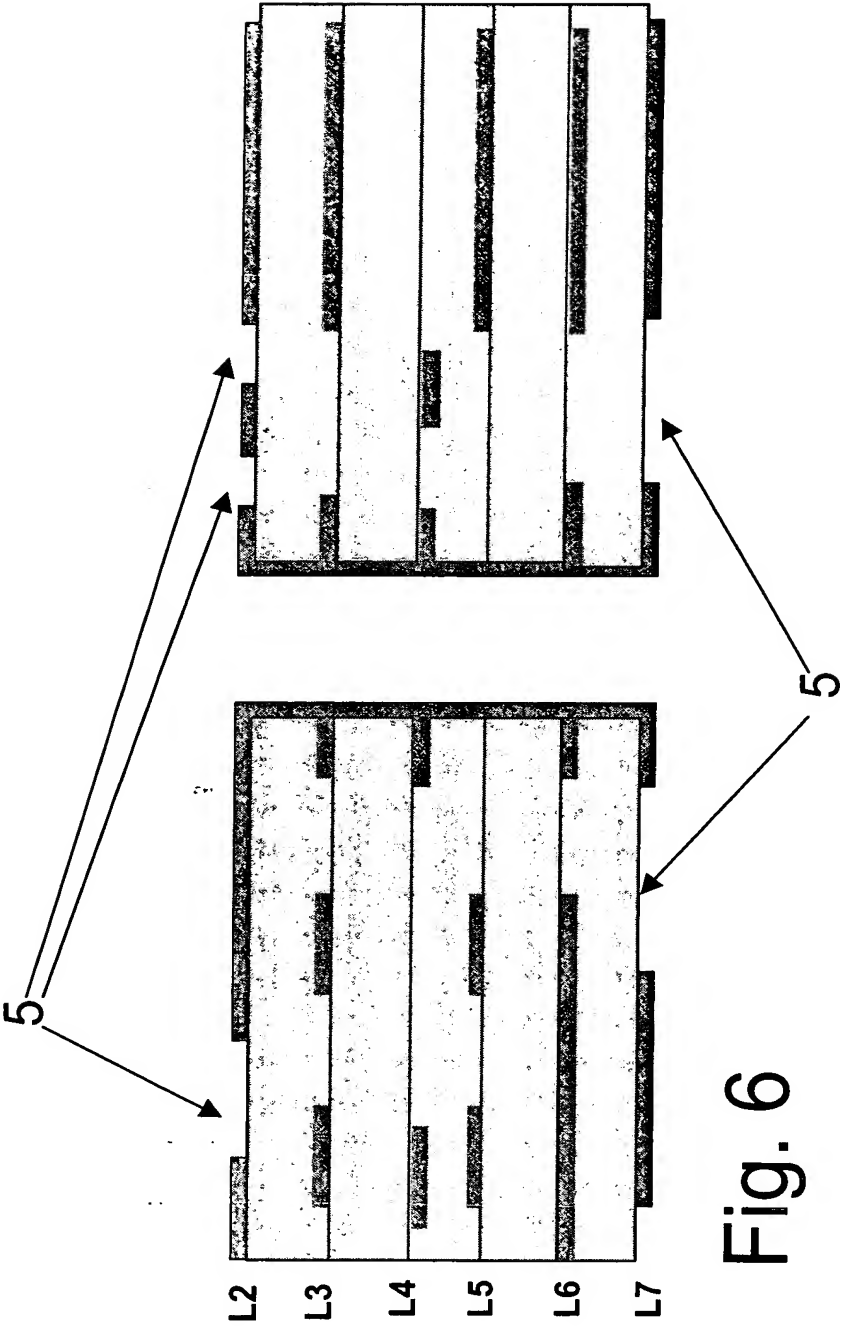


Fig. 6

Formation of the microvia layers, i.e. the outer layers

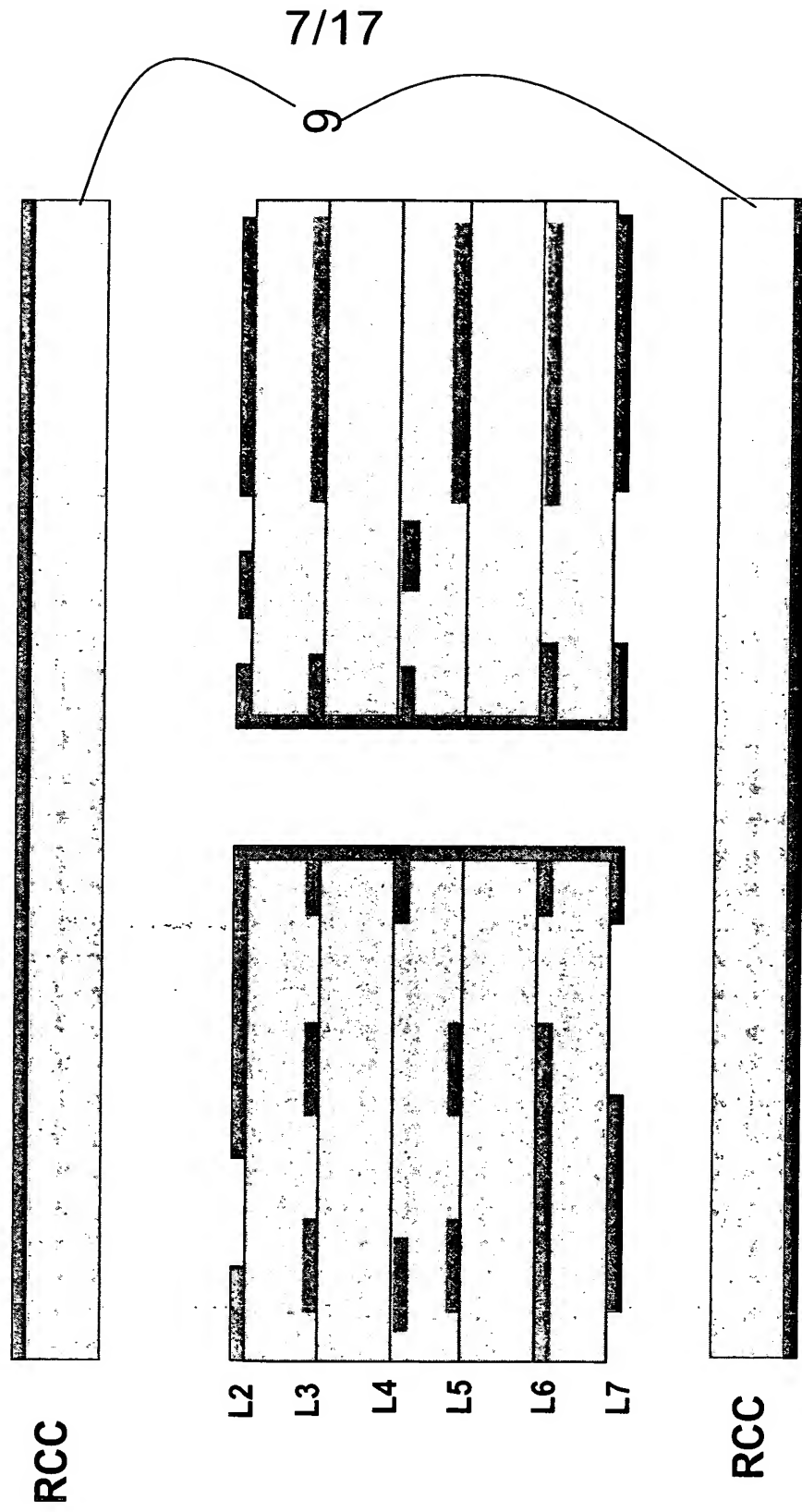


Fig. 7

Pressing together

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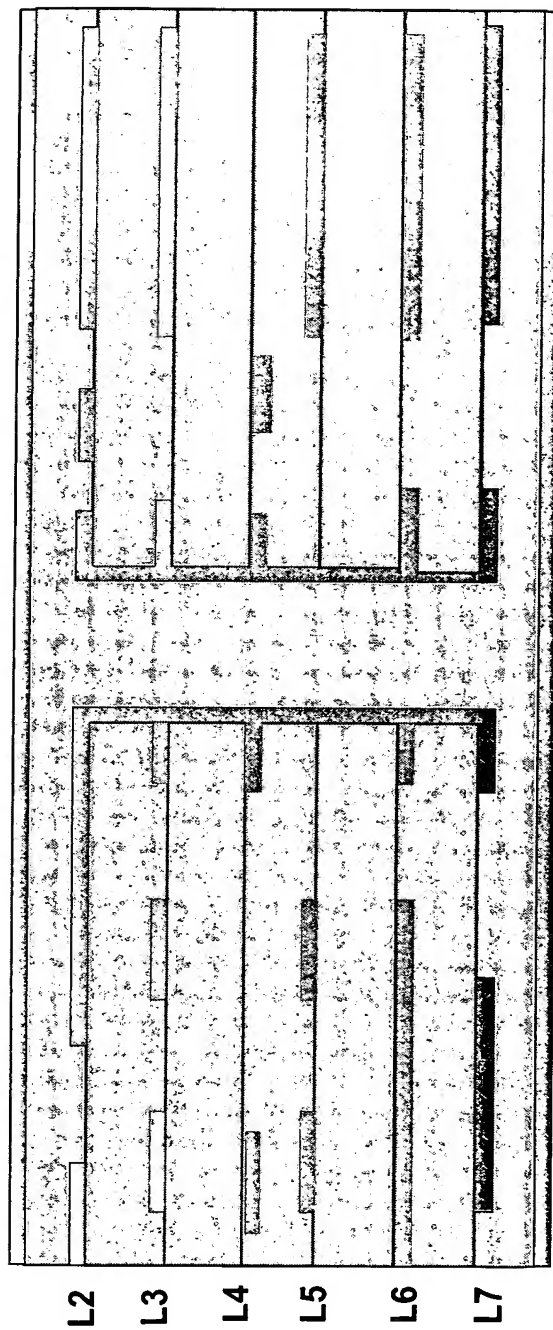


Fig. 8

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# Through-drilling

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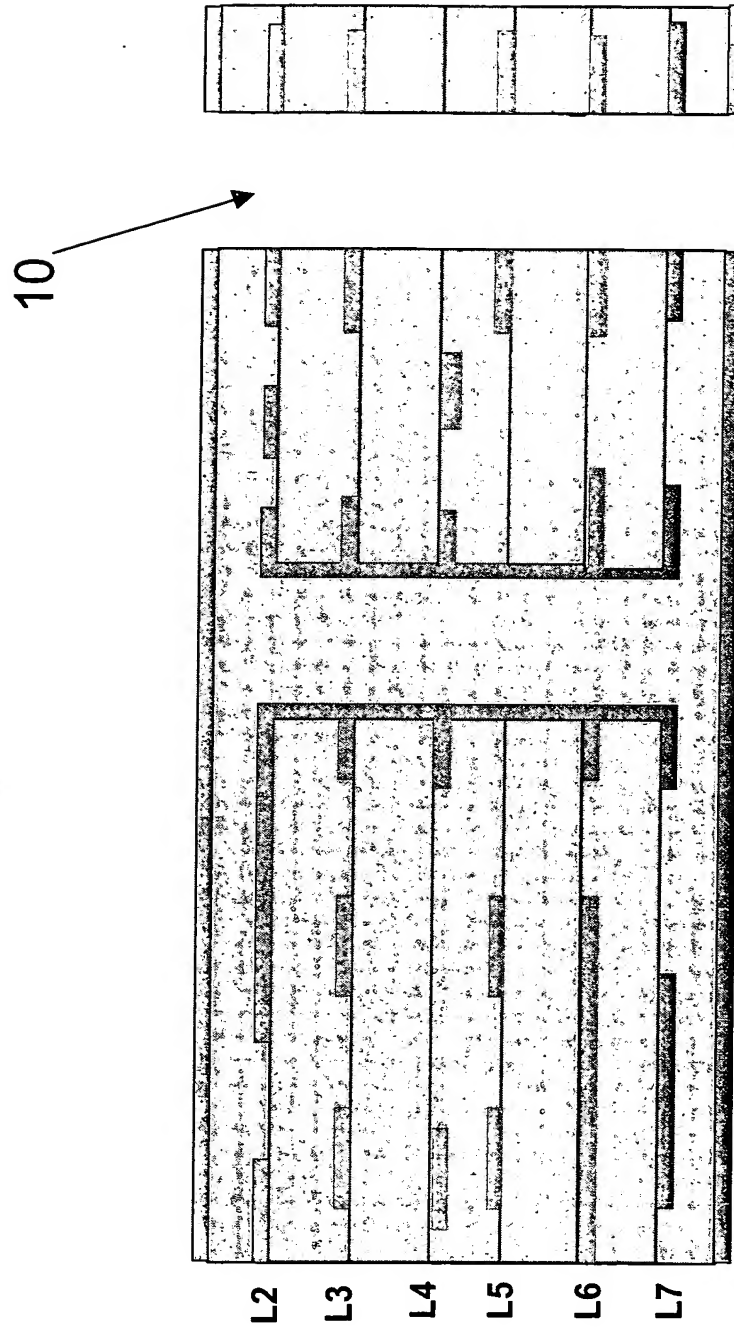
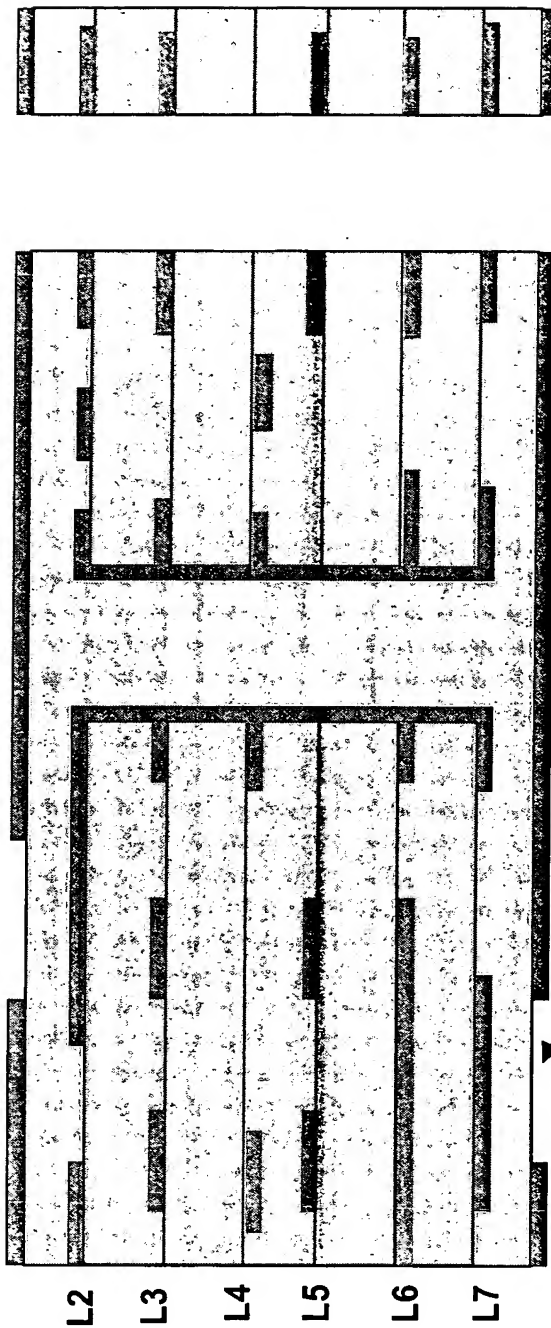


Fig. 9

Pre-etching to laser

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Fig. 10

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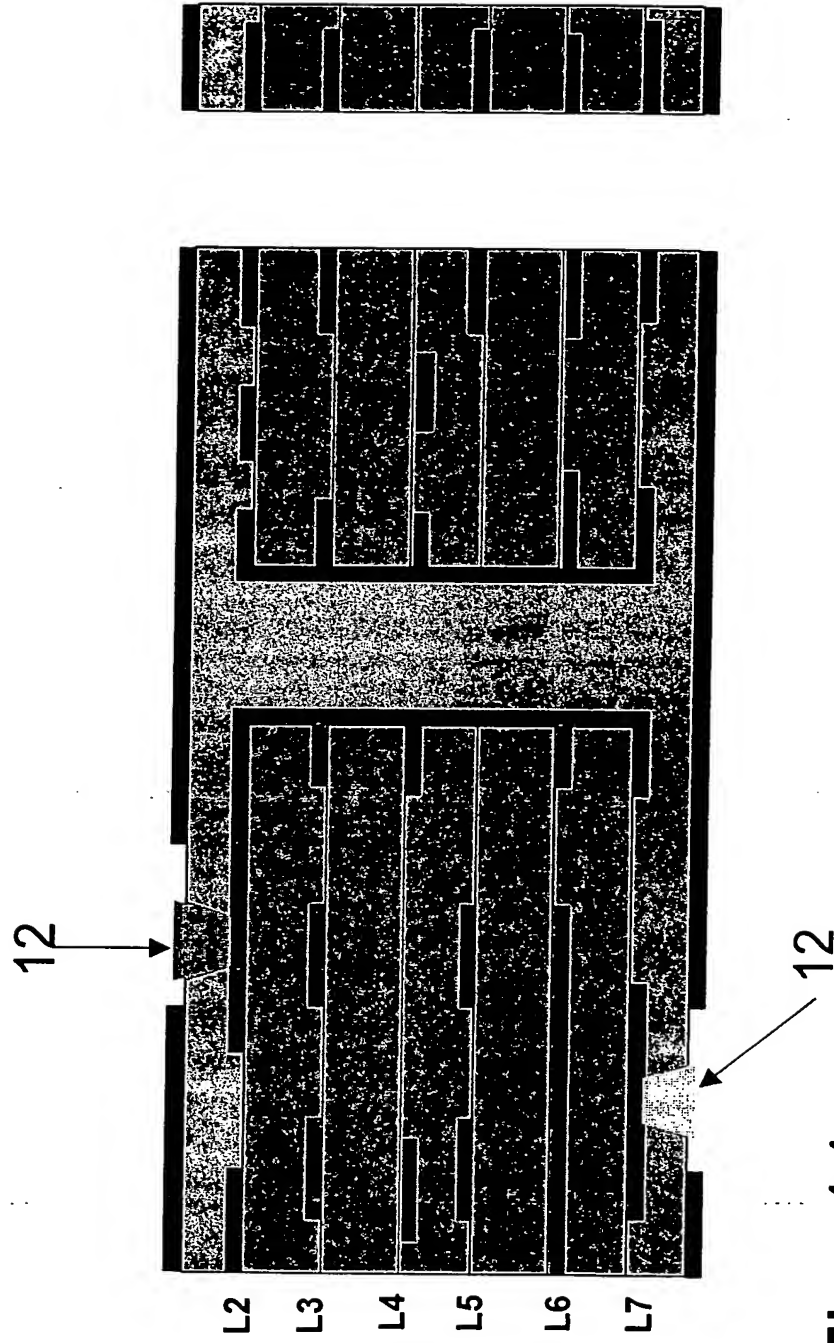


Fig. 11

Plating of the through holes to make them  
conducting

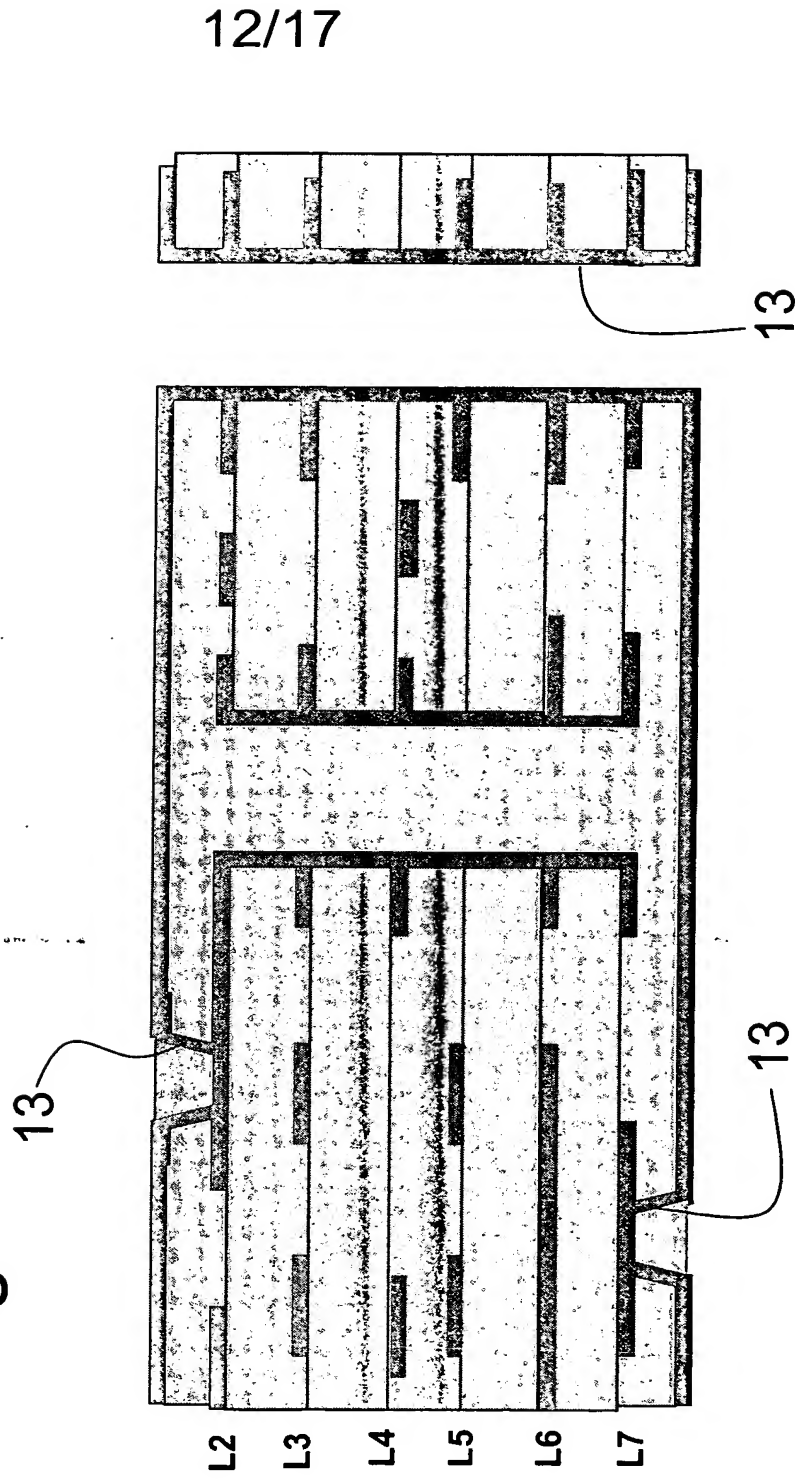


Fig. 12

# Formation of the conducting pattern

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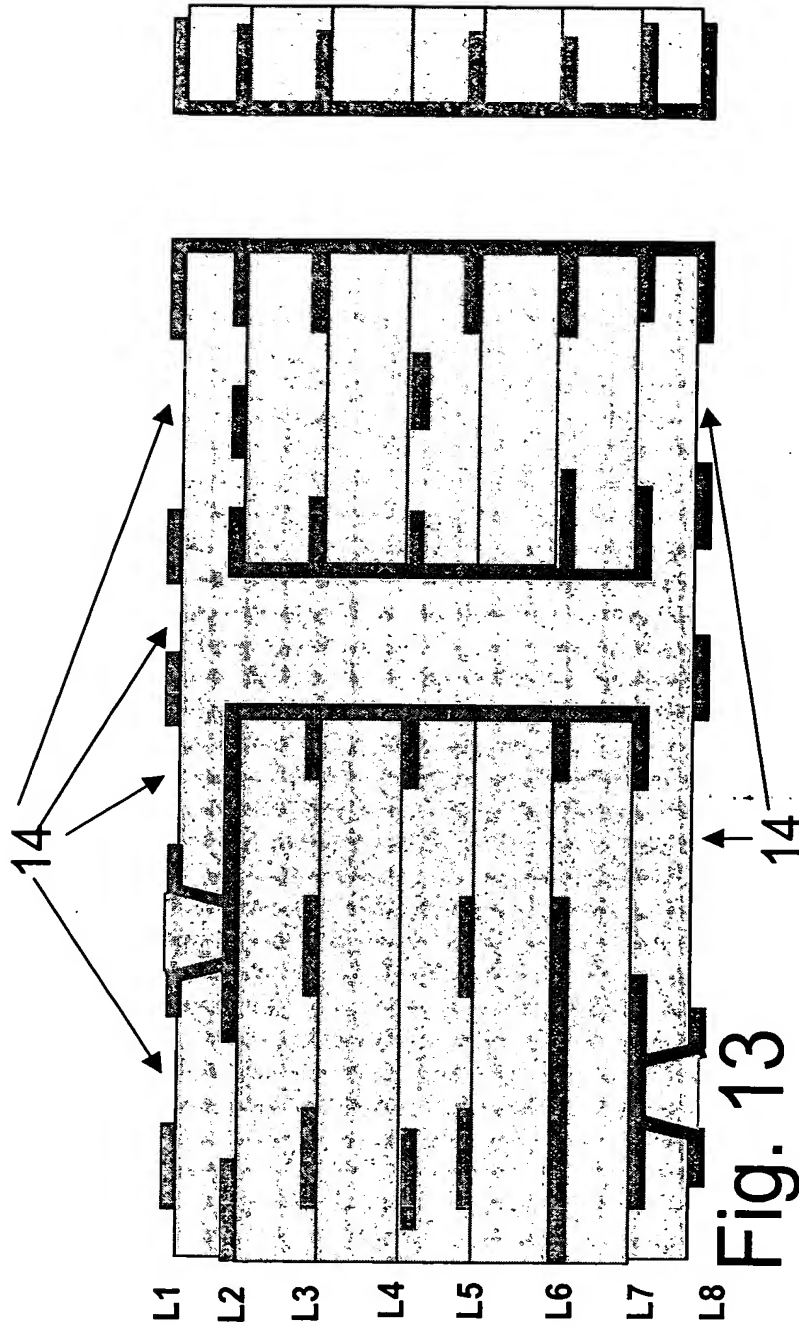


Fig. 13

# Protective surfacing for soldering

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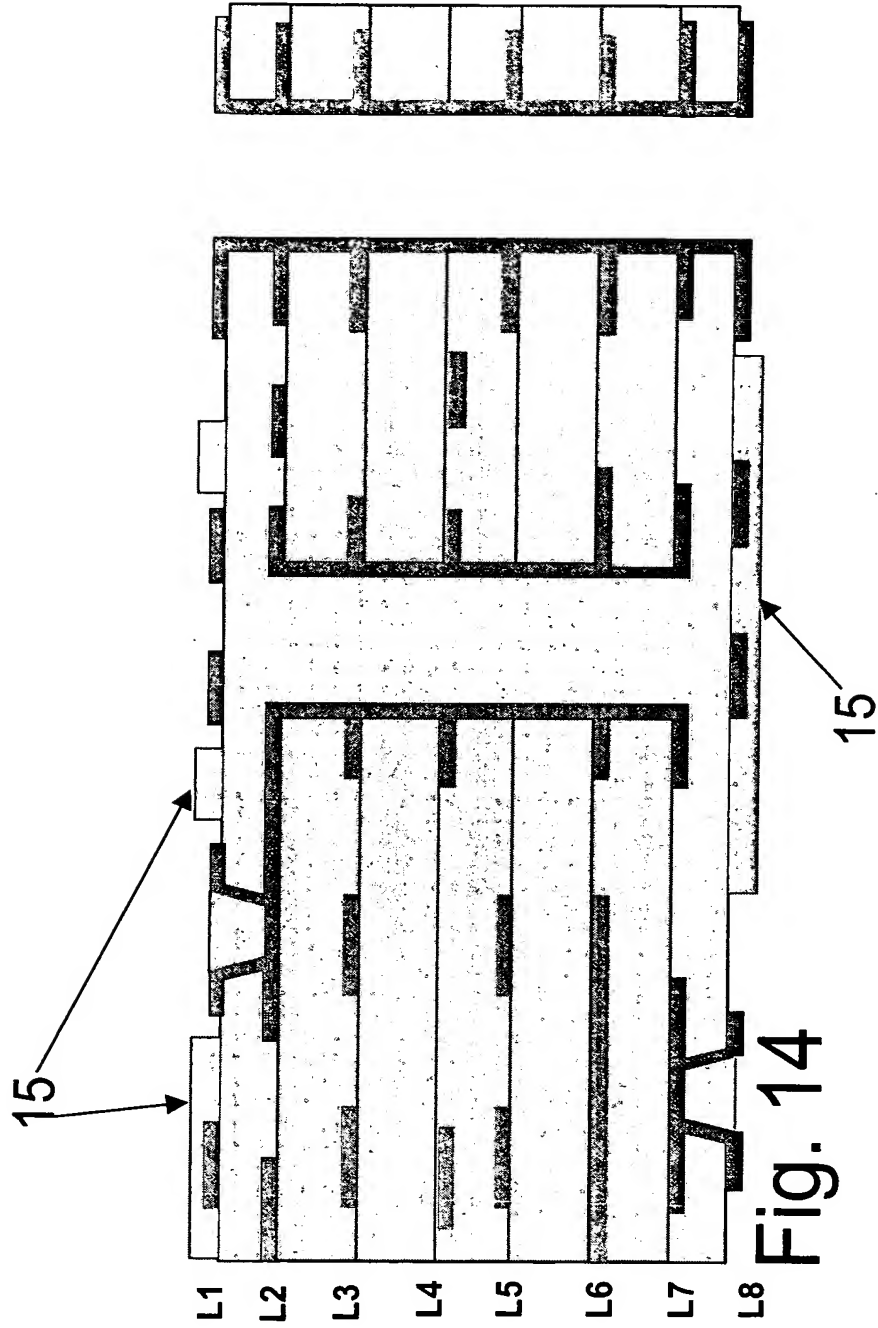
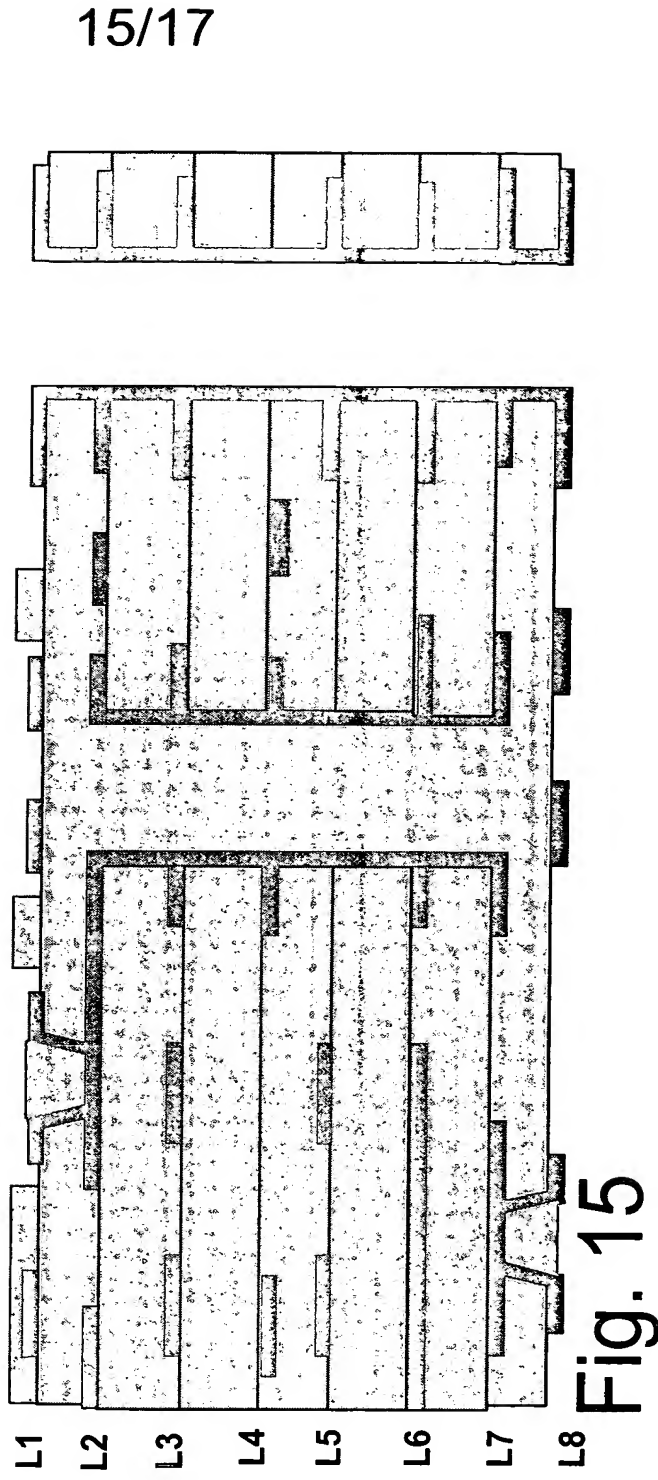


Fig. 14

# 8-layer microvia plate containing buried through holes



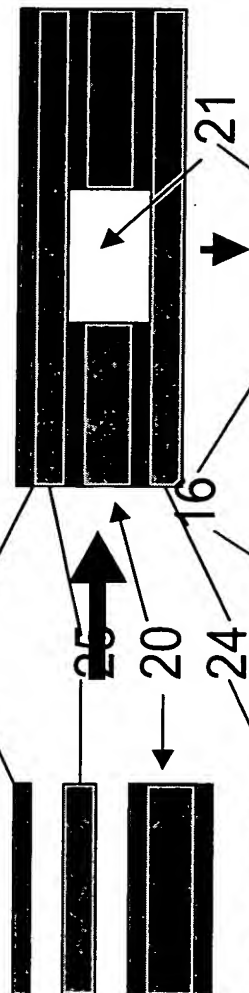




# LAMINATE SPEAKER

Fig. 17b

Buried hole drilling and pressing



Cu 18 um  
Aramid 50 um  
FR4 core 100 um  
18/18um Cu  
(C-stage)  
FR4 prepreg  
(nf)

Fig. 17c

Copper donut plating 15 um

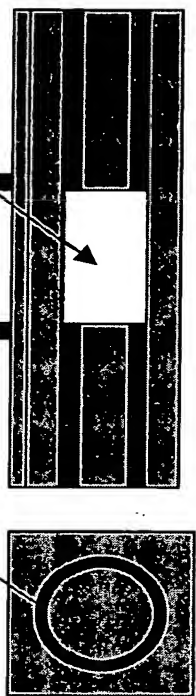


Fig. 17d

uVia drilling

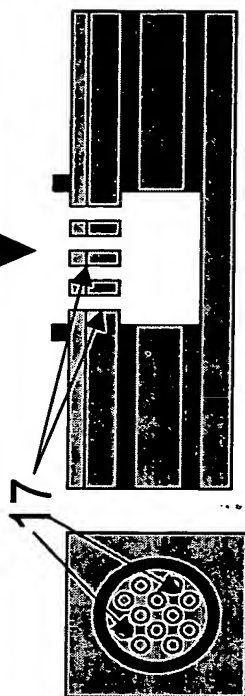


Fig. 17e

Adding metalcoated foil, not insulated from Cu-donut

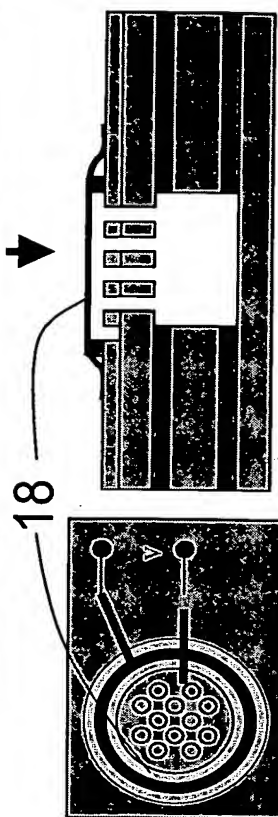


Fig. 17a



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